

ABSTRACT OF THE DISCLOSURE

A hardware management apparatus of this invention has at least one semiconductor integrated circuit component which includes a plurality of semiconductor components, and a first memory for storing first management information used to manage the mount states of the plurality of semiconductor components, at least one circuit board which includes the at least one semiconductor integrated circuit component, and a second memory for storing second management information used to manage the mount state of the at least one semiconductor integrated circuit component, and a third memory for storing third management information used to manage the mount state of the at least one circuit board, and rewrites the corresponding first management information to third management information when the mount state of hardware is changed, thus managing the hardware.